

CTHPC 2026

Call For Papers

The 31st Workshop on Compiler Techniques and System Software for High-Performance and Embedded Computing (CTHPC 2026)

May 28-29, 2026
NYCU, Hsinchu,
Taiwan

Theme

Embracing High-Performance Computing Innovations:

Compiler, Computer Architecture, Operating System, and Embedded System

Host



NYCU

KeynoteSpeakers



Compiler Techniques on Reliability and Security

Dr. Changhee Jung
– Purdue University, USA



Compiler Techniques on Innovative Applications

Dr. Tobias Grosser
– University of Cambridge, UK

Topics

- **Compiler and Programming Language**
 - Compilers for innovative processors and AI applications
 - GPGPU programming and parallelizing/vector compilers
- **High-Performance Computing**
 - Data-intensive applications
 - Scientific applications
- **Operating System**
 - Memory and storage systems
 - Virtualization and hypervisor design, security/privacy, and reliability in OS
- **Embedded System**
 - Edge computing and cyber-physical systems
 - Real-time systems

Important Dates

Paper Submission Deadline

April 6, 2026

Notification of Acceptance

May 1, 2026

Camera-Ready Paper Submission

May 8, 2026

Early Bird Registration Deadline

May 10, 2026

Regration Period

March 1 - May 15, 2026

Executive Committees

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Li-Pin Chang, NYCU
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Ding-Yong Hong, Academia Sinica

Conference Website

<https://reurl.cc/oKlkav>

Registration Website

<https://reurl.cc/rKpy21>

Paper Submission Link

<https://reurl.cc/eVqRej>

Supported by



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